



In re Patent Application of:

Kishore K. Chakravorty

Title:

ELECTRONIC ASSEMBLY COMPRISING INTERPOSER WITH EMBEDDED CAPACITORS AND

METHODS OF MANUFACTURE

Attorney Docket No.: 884.267US1

## PATENT APPLICATION TRANSMITTAL

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

## **BOX PATENT APPLICATION**

Commissioner for Patents Washington, D.C. 20231

We are transmitting herewith the following attached items and information (as indicated with an "X"):

Х Return postcard.

X Utility Patent Application under 37 CFR § 1.53(b) comprising:

Specification (25 pgs, including claims numbered 1 through 30 and a 1 page Abstract).

L. X Formal Drawing(s) (<u>8</u> sheets).

M X Signed Combined Declaration and Power of Attorney ( <u>3 pgs</u>).

N Check in the amount of \$1,104.00 to pay the filing fee. X

Assignment of the invention to Intel Corporation (2 pgs) and Recordation Form Cover Sheet. X

Check in the amount of \$40.00 to pay the Assignment recording fee. X

X Information Disclosure Statement (1 pgs), Form 1449 (2 pgs), and copies of cited references (16).

The filing fee has been calculated below as follows:

22 2	No. Filed	No. Extra	Rate	Fee
TOTAL CLAIMS	30 - 20 =	10	x 18 =	\$180.00
ÎNDEPENDENT CLAIMS	6 - 3 =	3	x 78 =	\$234.00
] MULTIPLE DEPENDENT CLAIMS PR	\$0.00			
BASIC FEE				\$690.00
TOTAL				\$1,104.00

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CFR 1.10 on the date indicated above and is addressed to the Commissioner for Patents, Bo	Patent Application, Washington, D.C. 20231.

Shawn L. Hise







## U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE FEE RECORD SHEET

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01 FC:101 02 FC:103 03 FC:102

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\*U.S. GPO: 1999-459-082/19144